

FIG. 1A

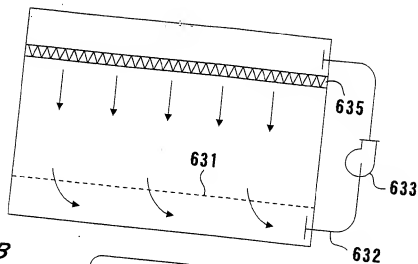


FIG. 1B

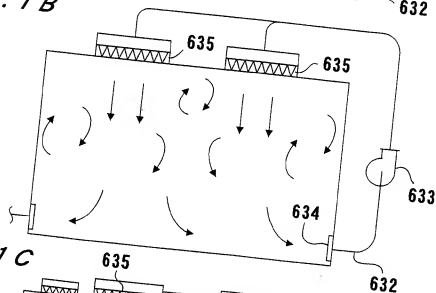
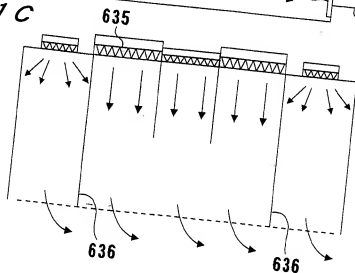


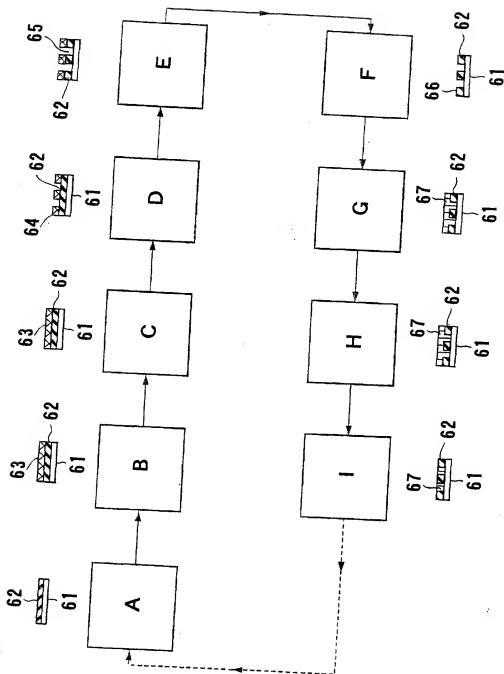
FIG. 1C



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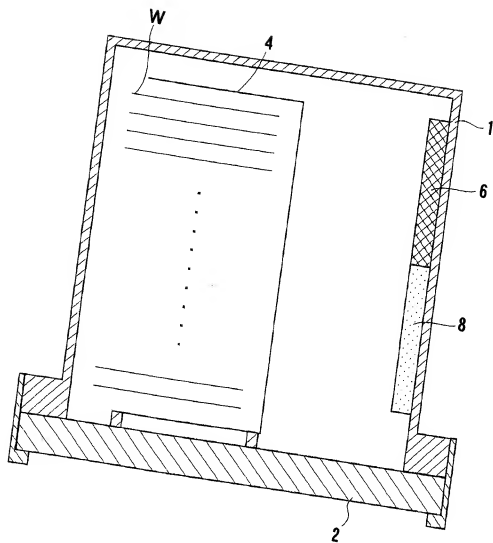
FIG. 2

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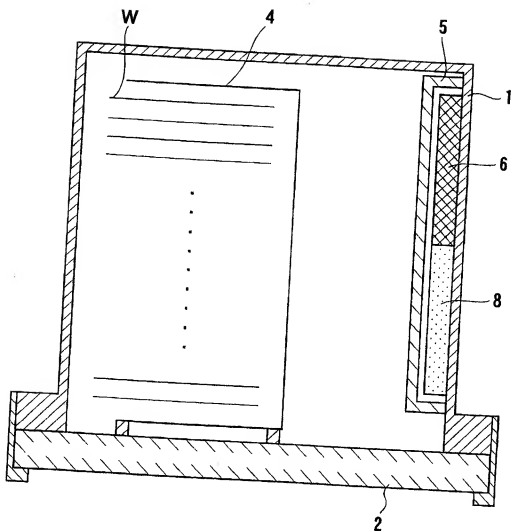
FIG. 3



10000304-120001

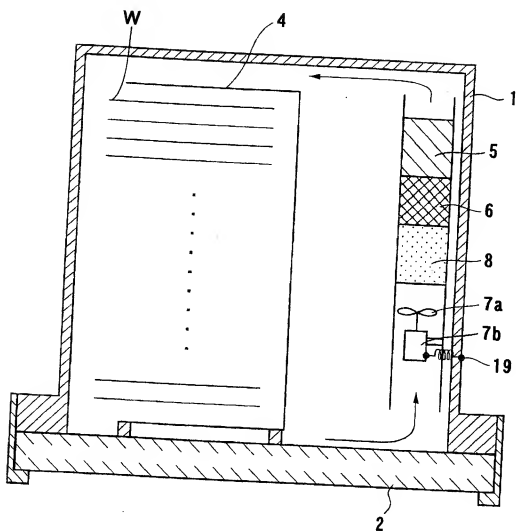
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FIG. 4



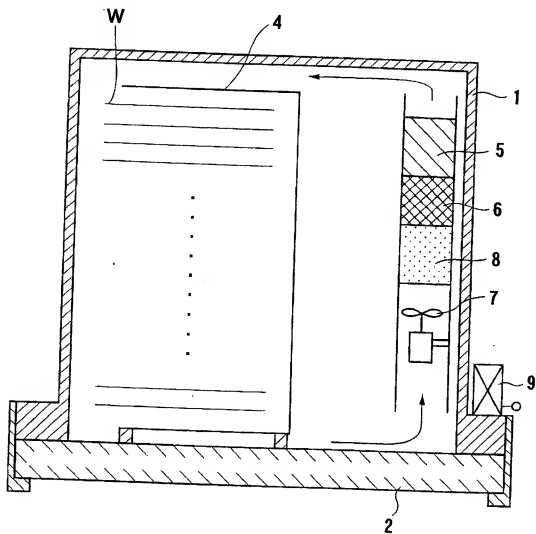
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FIG. 5



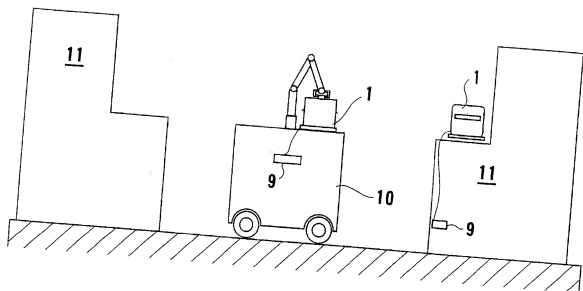
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FIG. 6



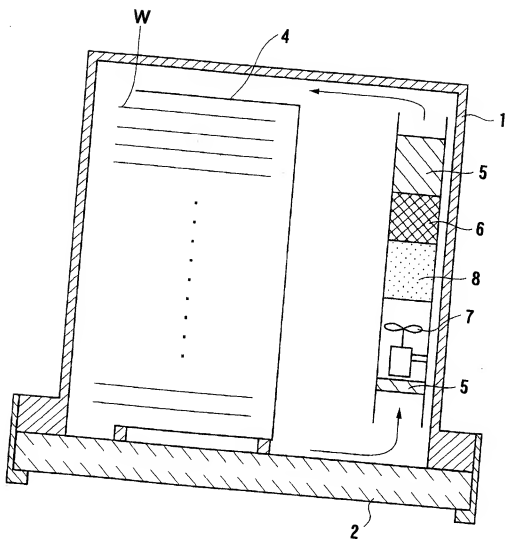
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FIG. 7



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FIG. 8



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FIG. 9

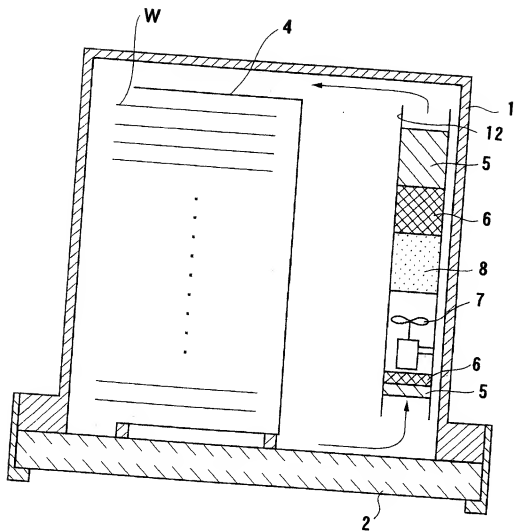
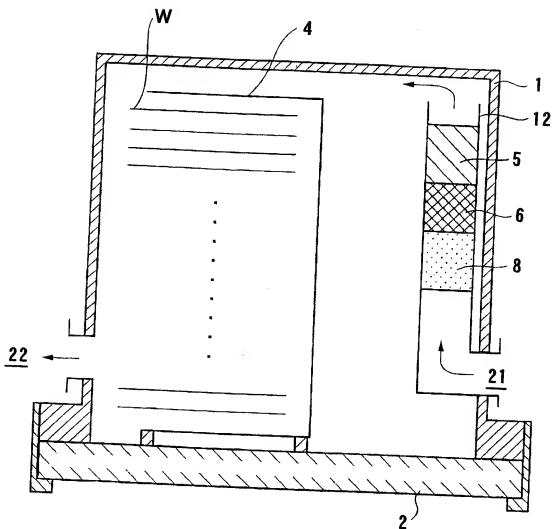
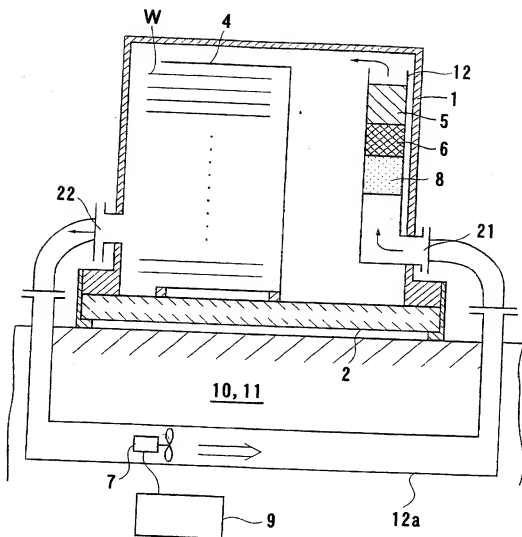


FIG. 10



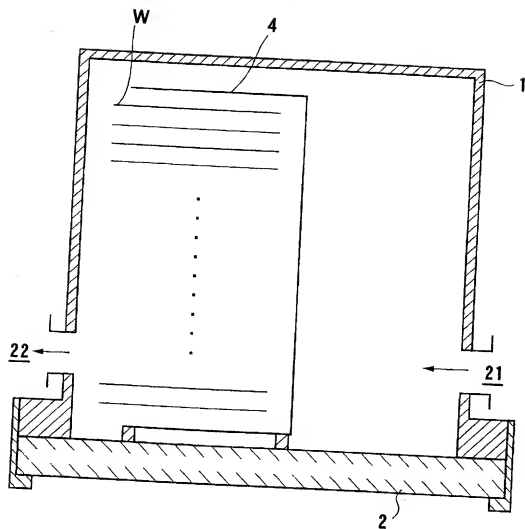
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FIG. 11



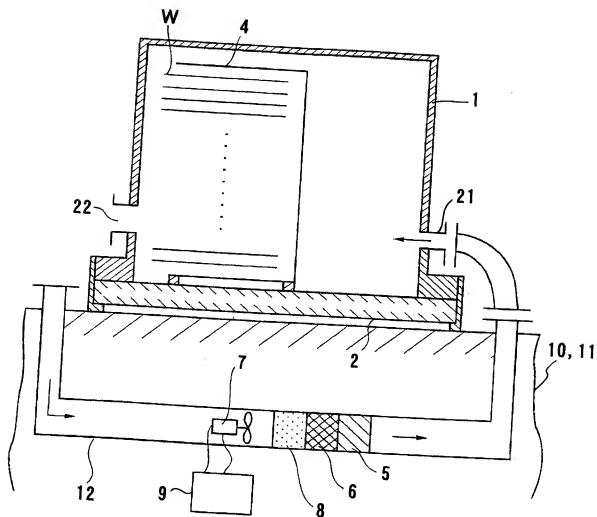
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FIG. 12



1000000-120401

FIG. 14



60034-10402

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FIG. 15

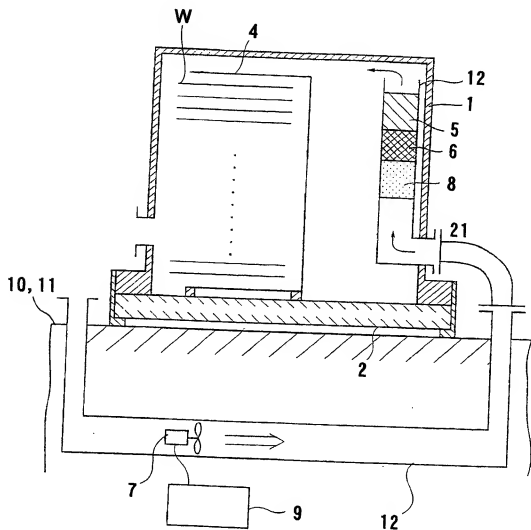
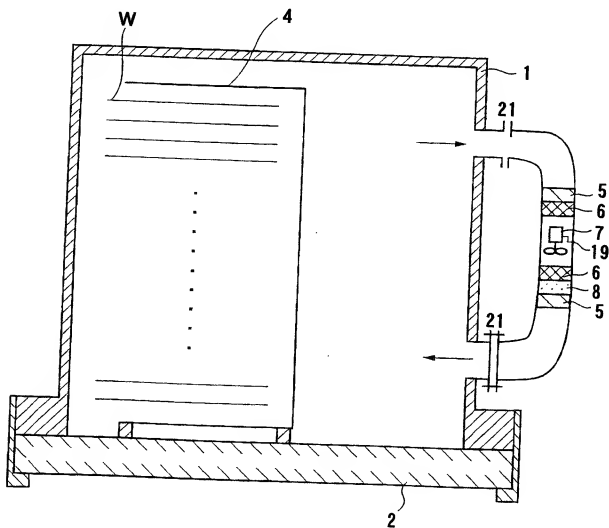
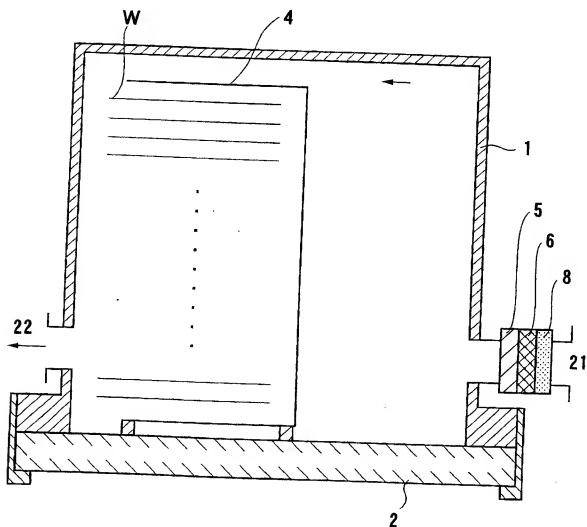


FIG. 16



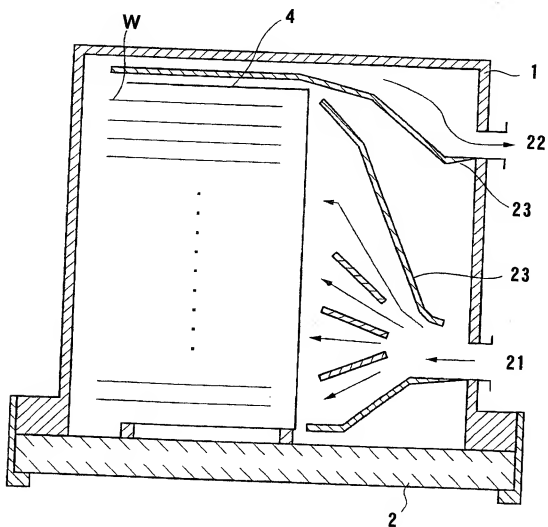
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FIG. 17



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FIG. 18



1600364-12494

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FIG. 19

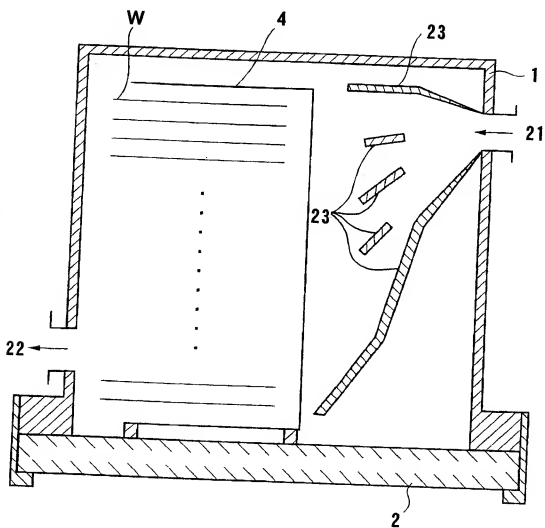


FIG. 20

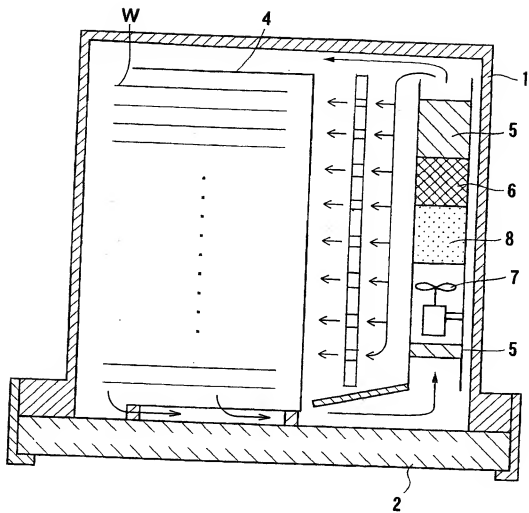


FIG. 21A

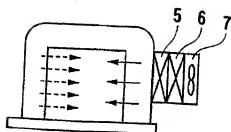
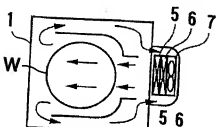


FIG. 21B

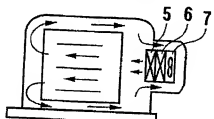
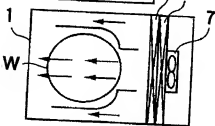


FIG. 21C

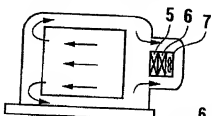
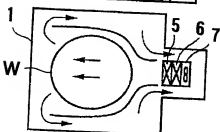


FIG. 21D

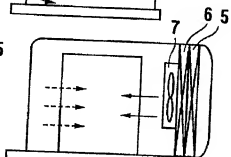
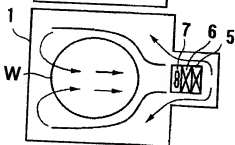


FIG. 21E

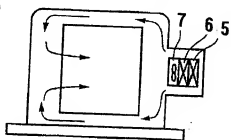
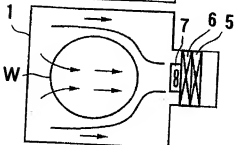
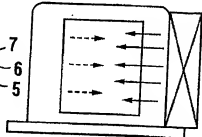
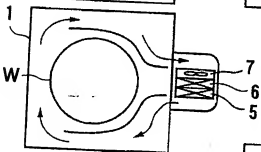
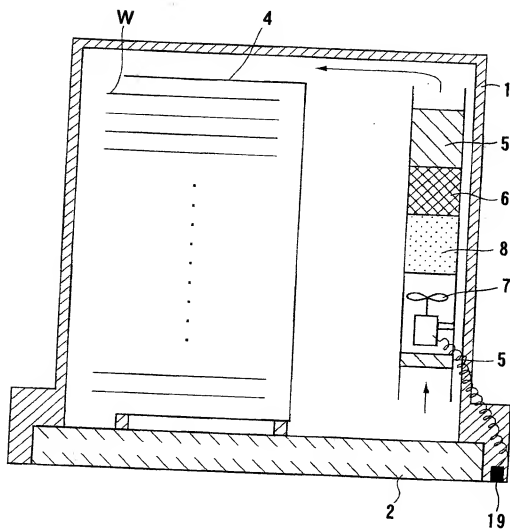


FIG. 21F



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FIG. 22



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FIG. 23A

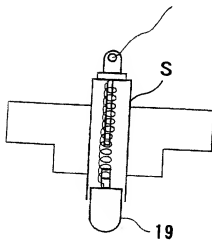
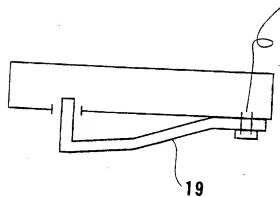
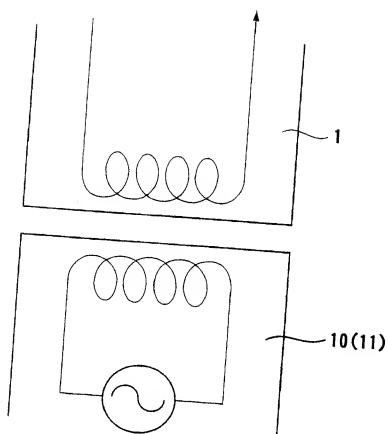


FIG. 23B



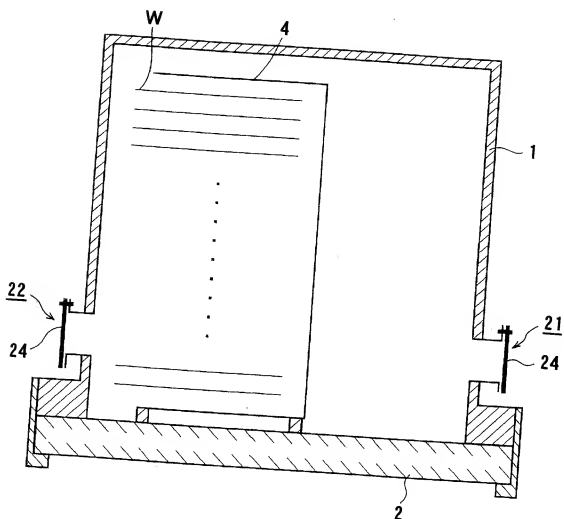
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FIG. 24



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FIG. 25



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FIG. 26

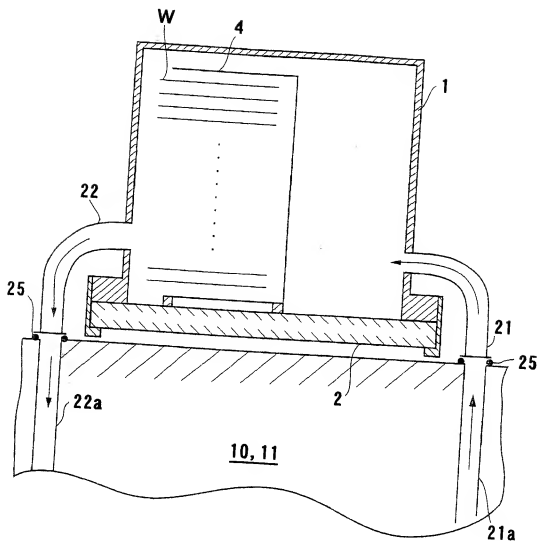


FIG. 27A

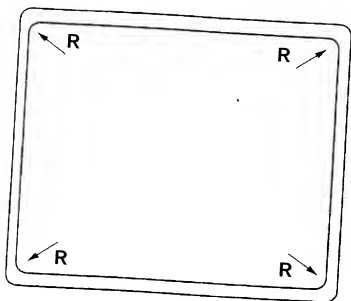


FIG. 27B

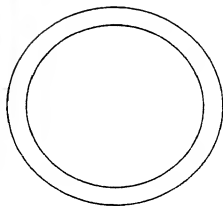
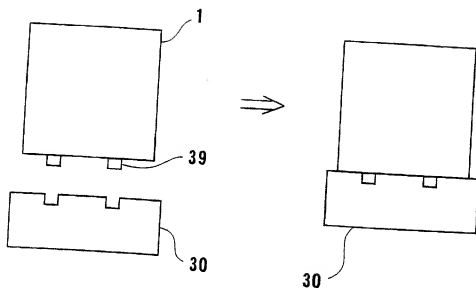


FIG. 27A

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FIG. 28



1000000 1000000

FIG. 29A

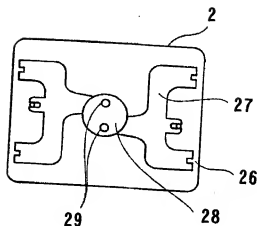


FIG. 29B

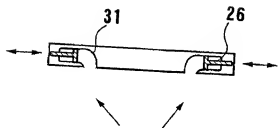


FIG. 29C



FIG. 29D

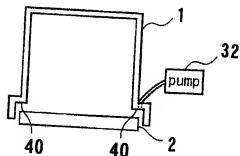


FIG. 29E

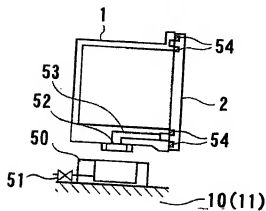
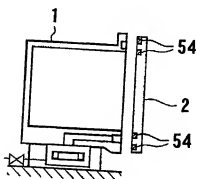


FIG. 29F



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FIG. 30

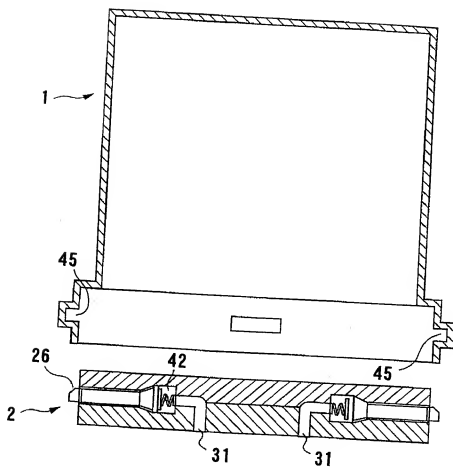


FIG. 31A

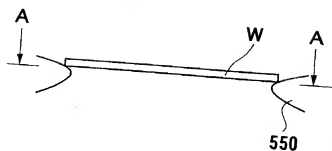


FIG. 31B

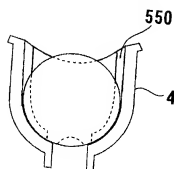


FIG. 32A

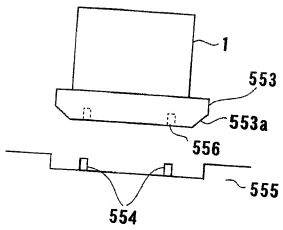


FIG. 32B

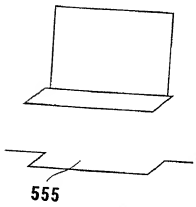


FIG. 32C

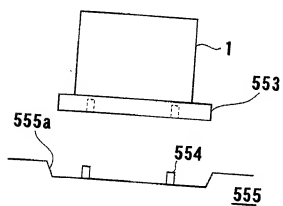


FIG. 33A

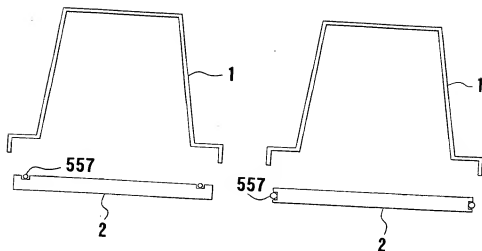


FIG. 33B

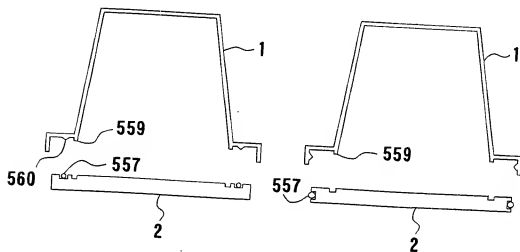


FIG. 34A

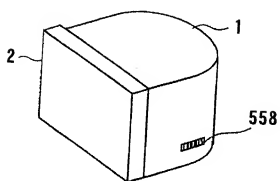


FIG. 34B

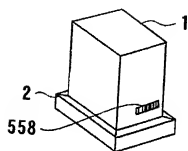


FIG. 35A

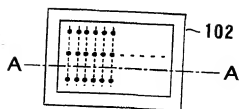


FIG. 35B

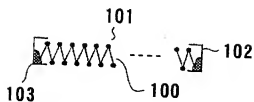


FIG. 35C

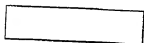


FIG. 36A

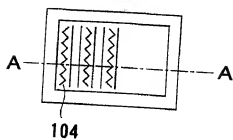


FIG. 36B

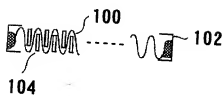
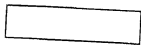
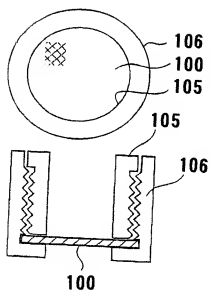


FIG. 36C



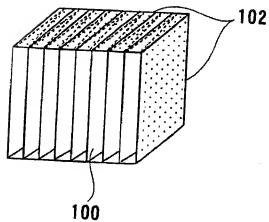
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FIG. 37



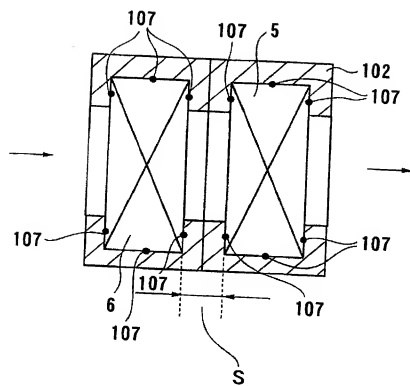
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FIG. 38



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FIG. 39



1000001-1000001

FIG. 40A

FIG. 40B

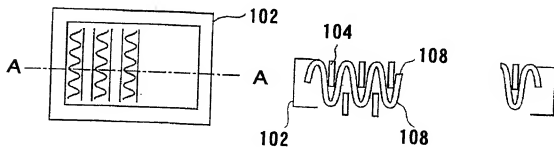
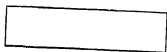


FIG. 40C



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FIG. 41



FIG. 41

FIG. 42A

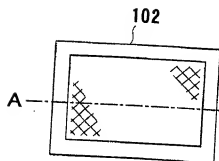


FIG. 42B

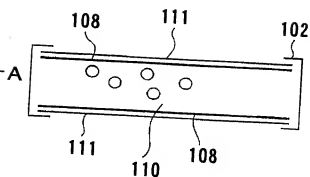


FIG. 42C

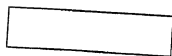


FIG. 1 is a perspective view of a rectangular device 109. The device has a front face with a series of circular elements 110 and a central rectangular opening 111.

FIG. 44A

FIG. 44B

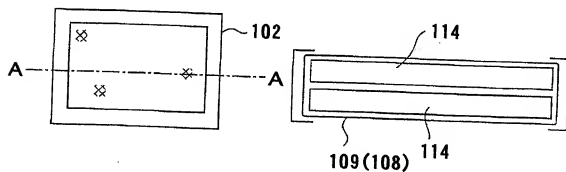


FIG. 44C

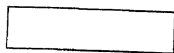


FIG. 45A

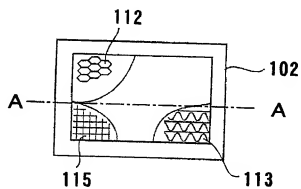


FIG. 45B

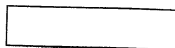


FIG. 46A

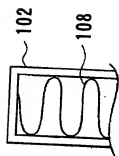


FIG. 46C

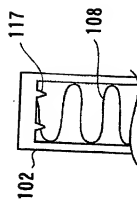
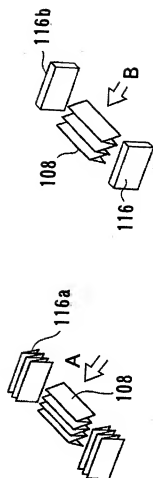
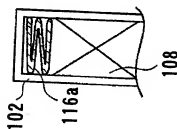


FIG. 46B



(A)



(B)

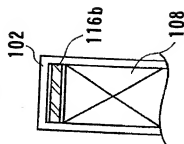


FIG. 47

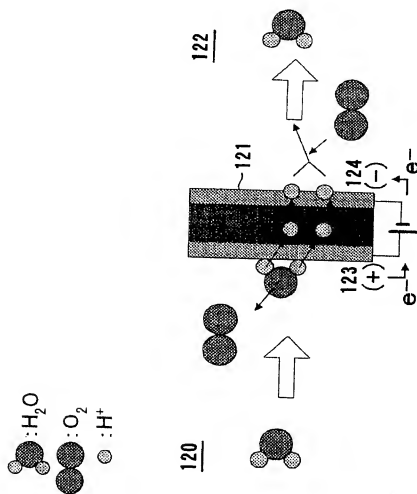


FIG. 48

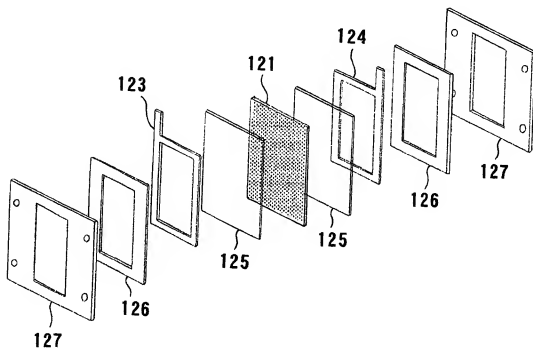
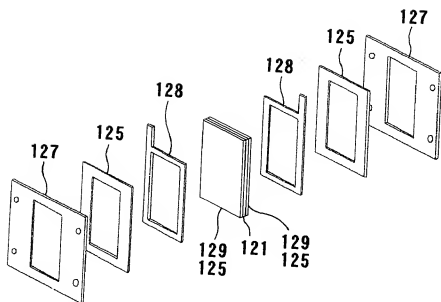


FIG. 48

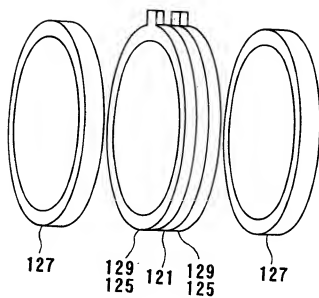
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FIG. 49



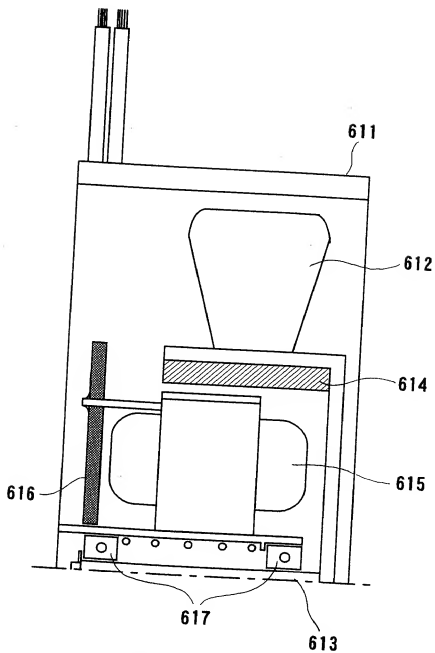
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FIG. 50



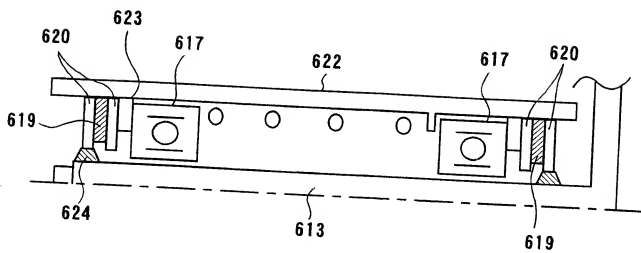
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FIG. 51



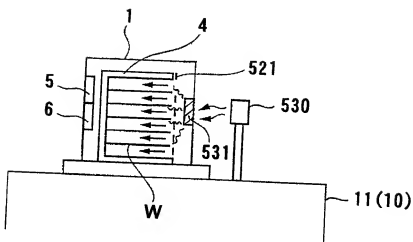
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FIG. 52



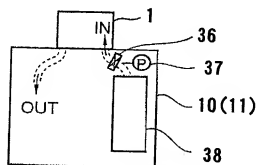
100-44388-100

FIG. 53



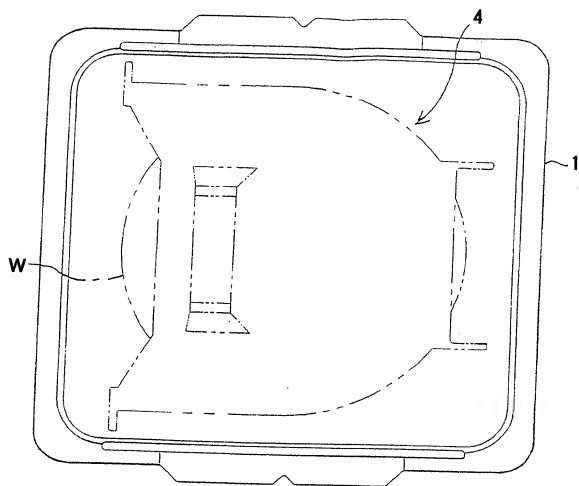
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FIG. 54



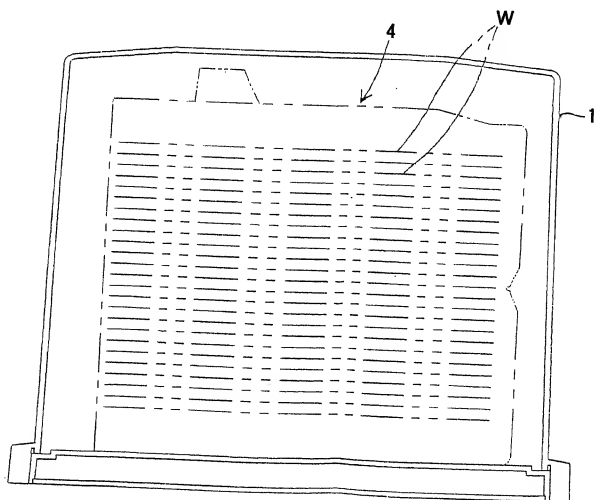
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FIG. 55



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FIG. 56



1000000 1000000

100007-1

FIG. 57A

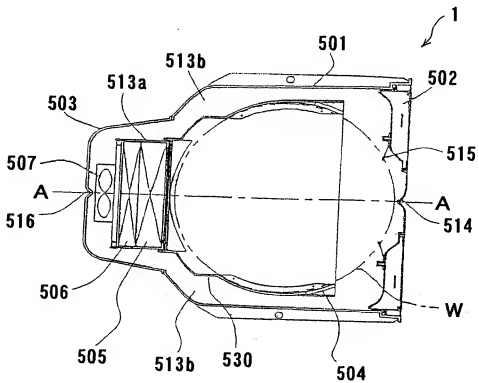
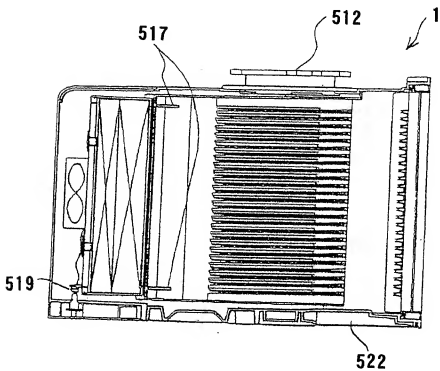


FIG. 57B



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FIG. 58A

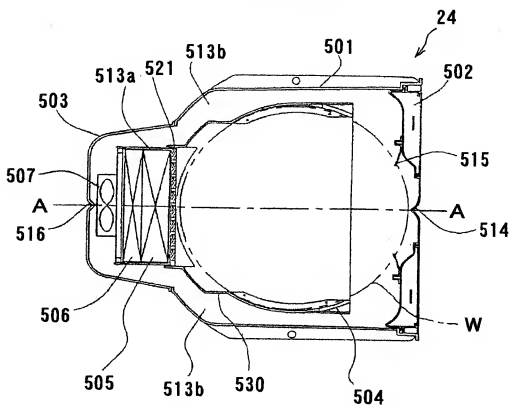
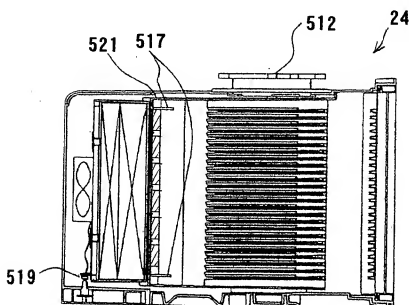


FIG. 58B



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FIG. 59A

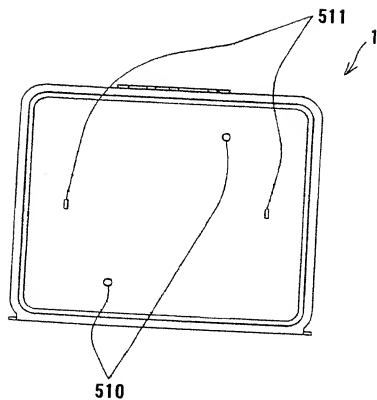
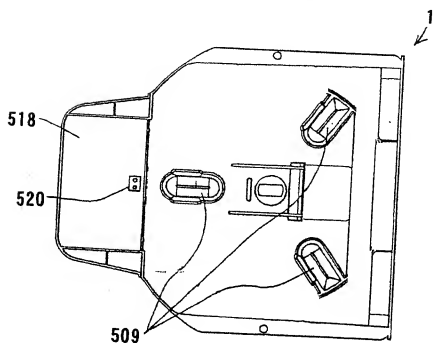


FIG. 59B



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FIG. 60A

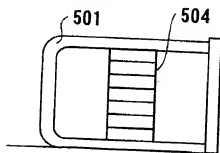


FIG. 60B

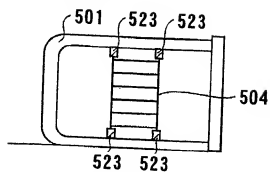


FIG. 60C

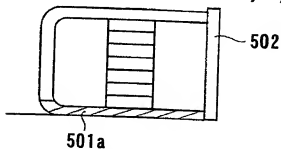


FIG. 60D

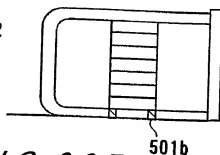


FIG. 60E

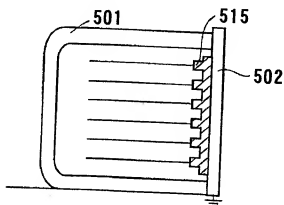


FIG. 60F

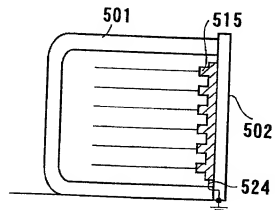


FIG. 60G

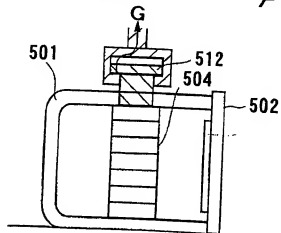
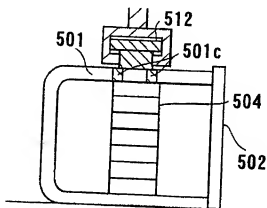
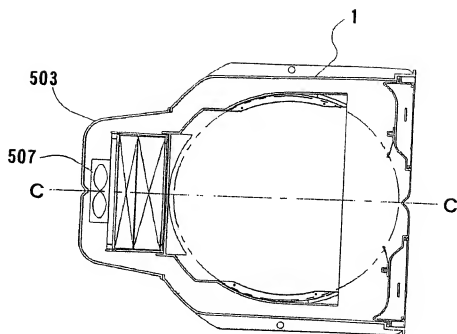


FIG. 60H



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FIG. 61



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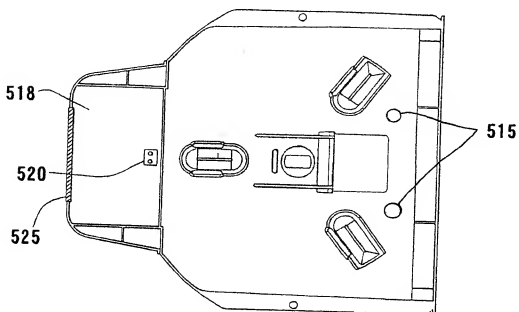
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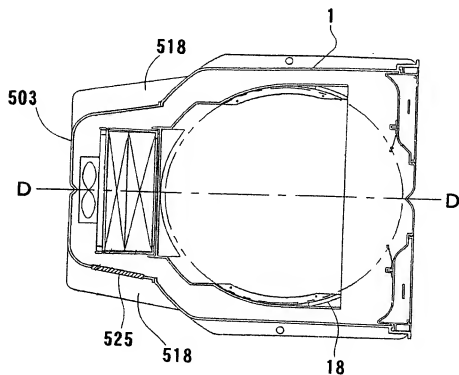
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FIG. 63



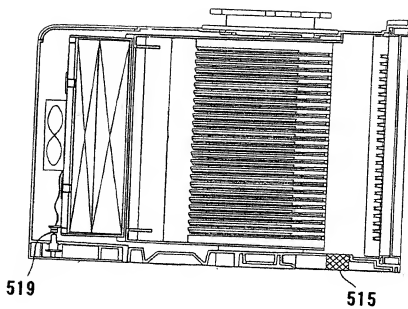
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FIG. 64



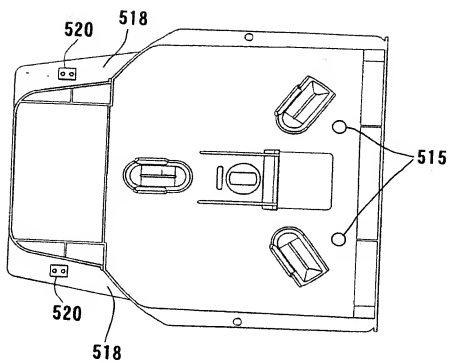
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FIG. 65



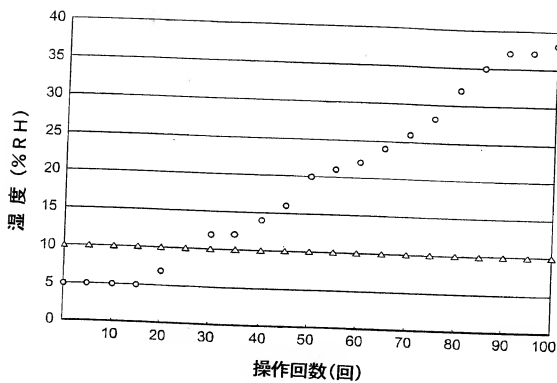
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FIG. 66



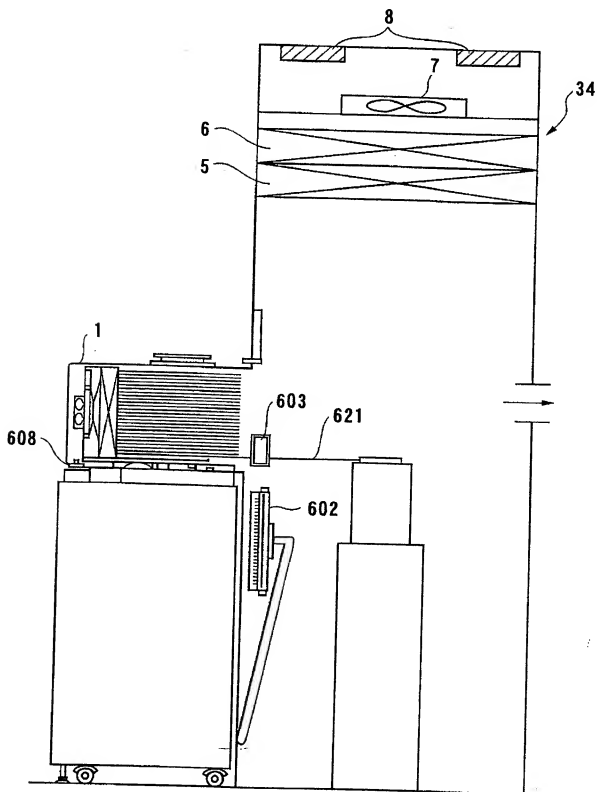
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FIG. 67



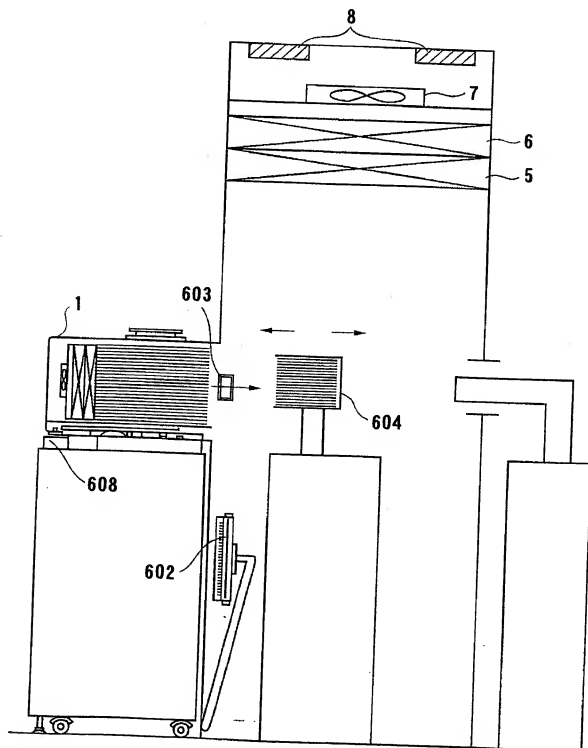
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FIG. 68



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FIG. 69



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FIG. 70A

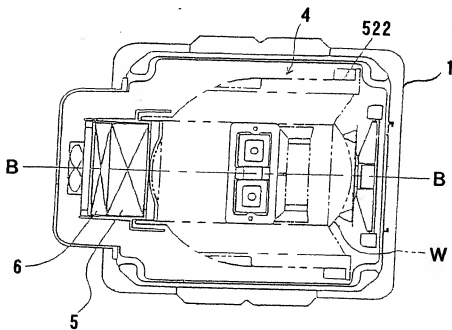
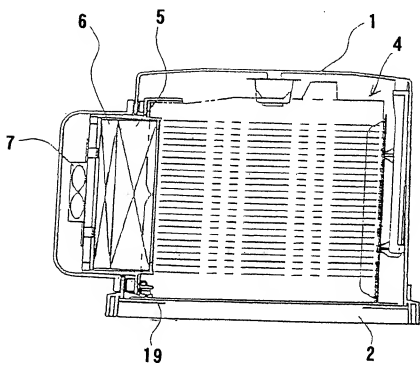


FIG. 70B



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FIG. 71A

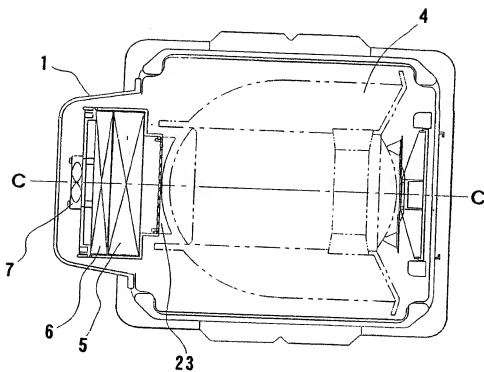
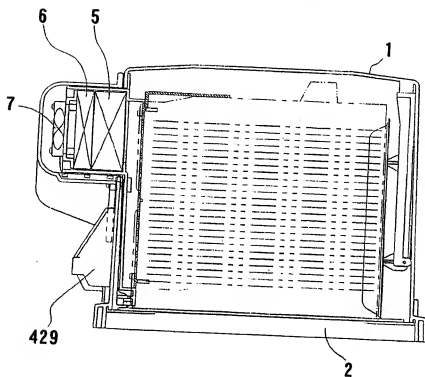
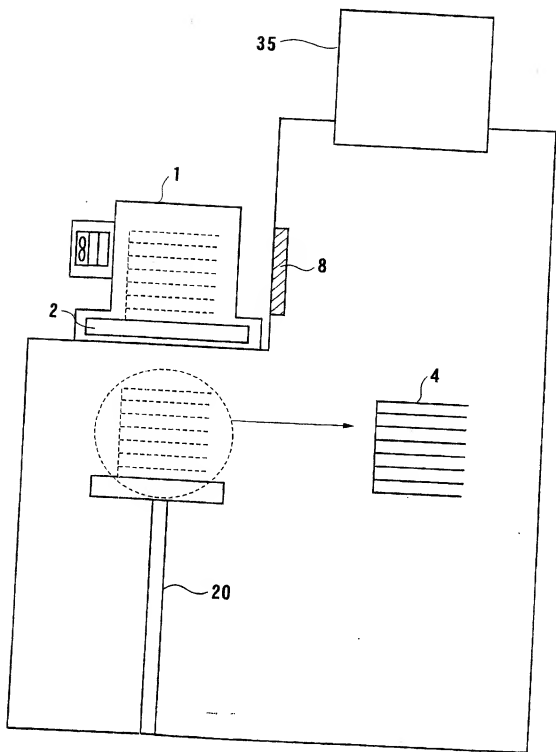


FIG. 71B



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FIG. 72



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FIG. 73

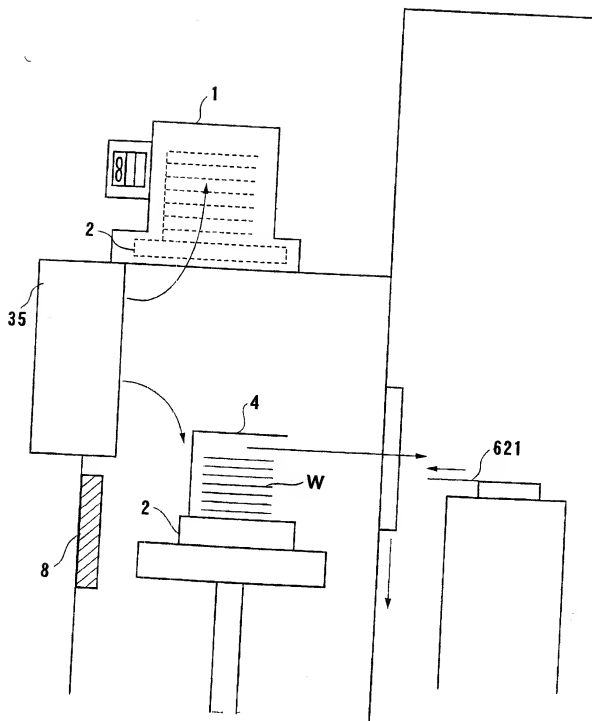
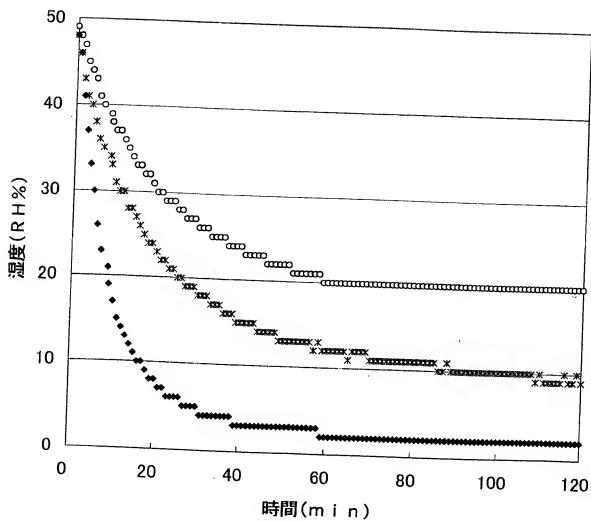


FIG. 74



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FIG. 75

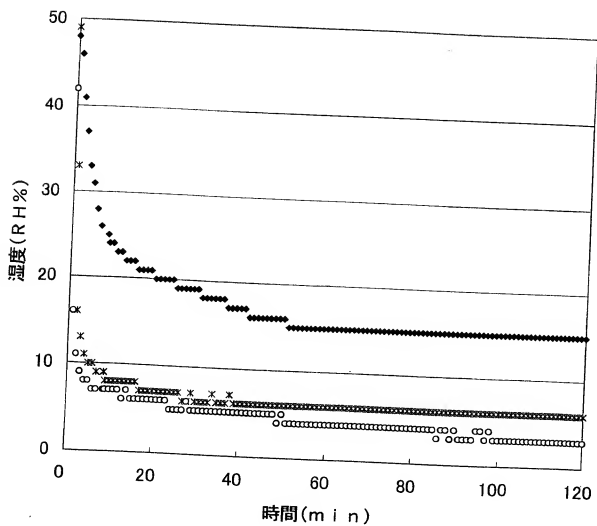


FIG. 76

半導体製造前工程

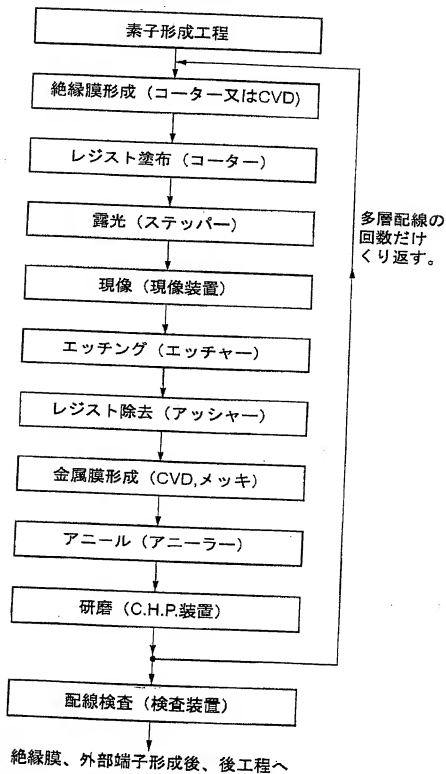


FIG. 77B

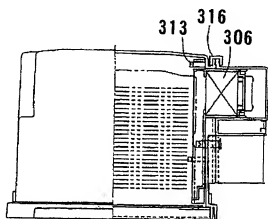


FIG. 77D

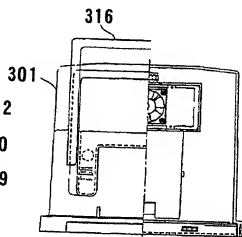


FIG. 78A

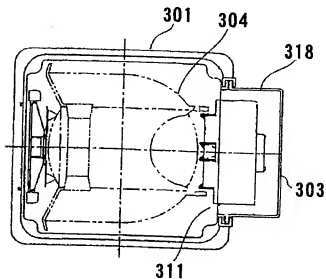


FIG. 78B

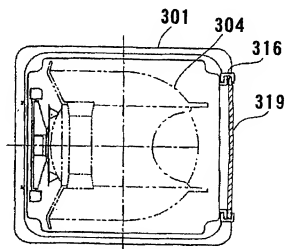


FIG. 78C

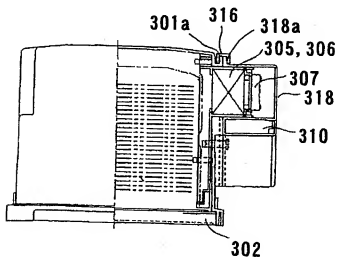


FIG. 78D

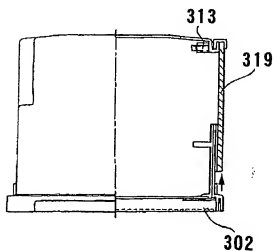


FIG. 79A

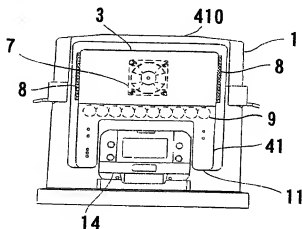


FIG. 79B

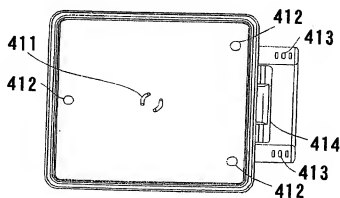
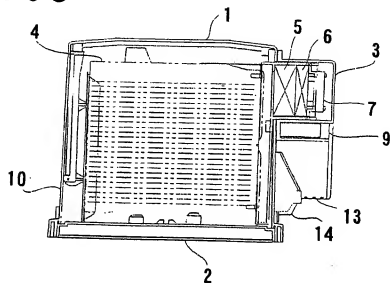


FIG. 79C



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FIG. 80A

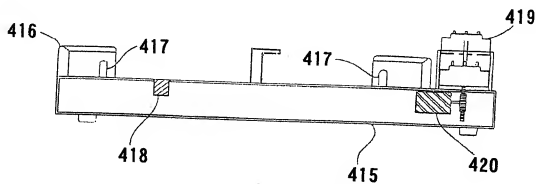
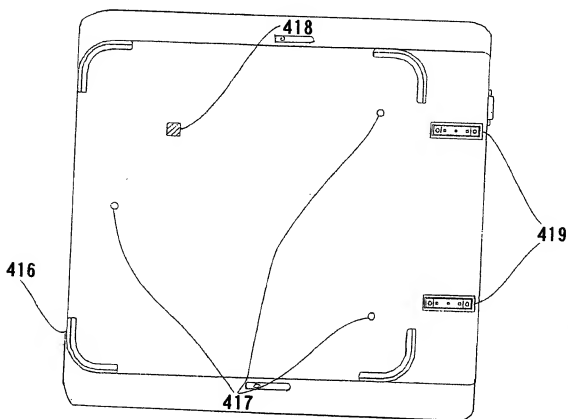


FIG. 80B



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FIG. 81A

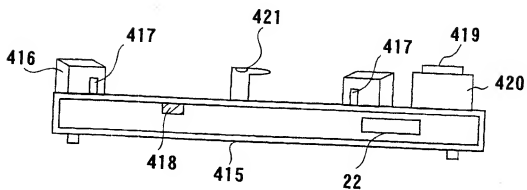
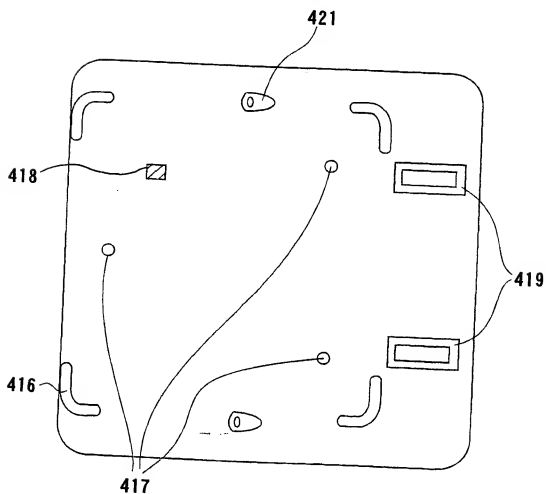
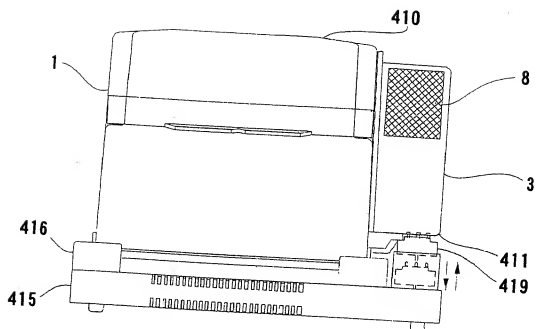


FIG. 81B



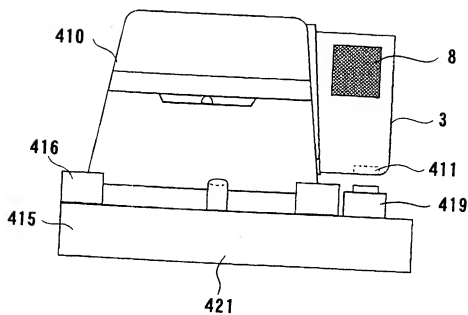
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FIG. 82



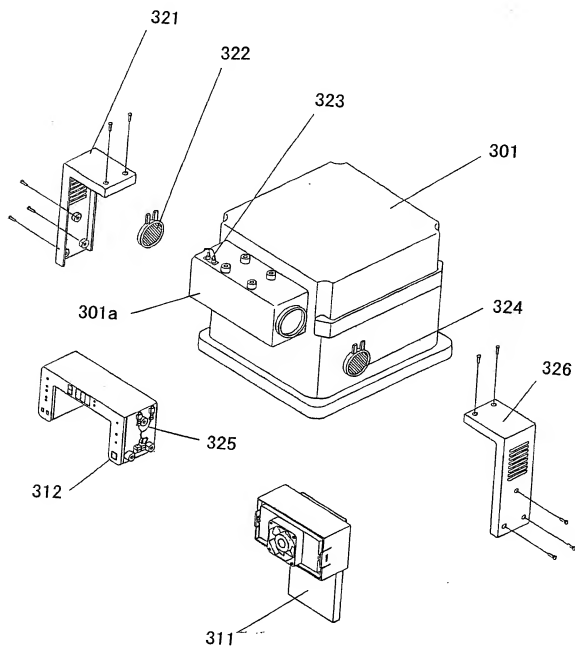
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FIG. 83



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FIG. 84



```
graph TD; A[測定結果入出力部] --> C[制御演算部]; B[運転制御信号入出力部] --> C; C <--> D[インターフェイス部]; E[メモリ] <--> C;
```

The diagram illustrates the control system architecture. It consists of five main components: a measurement result input/output unit (測定結果入出力部), a control signal input/output unit (運転制御信号入出力部), a control calculation unit (制御演算部), an interface unit (インターフェイス部), and a memory unit (メモリ). The control calculation unit is the central hub, receiving data from the measurement unit and sending signals to the control signal unit. It also interacts bidirectionally with the interface unit and the memory unit.

FIG. 86

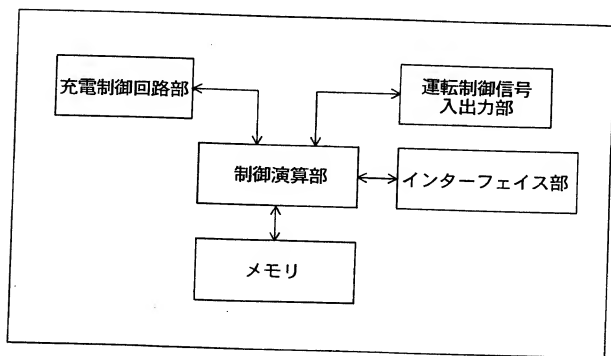


FIG. 87A

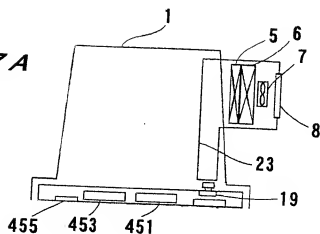


FIG. 87B

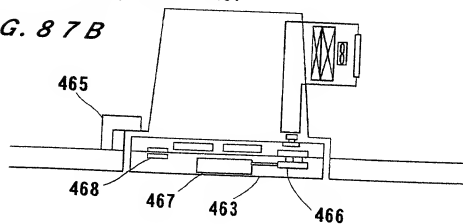
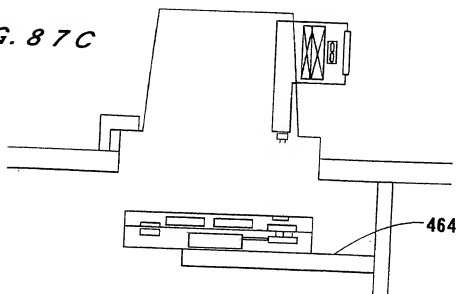


FIG. 87C



F I G. 88B

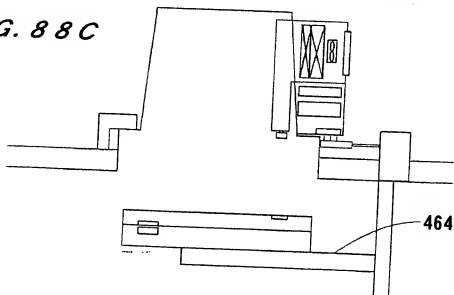
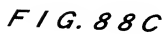
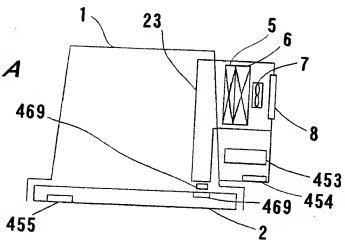
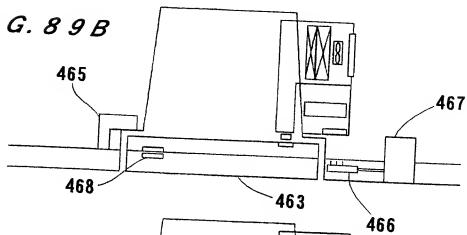


FIG. 89A



F I G. 89 B



F I G. 89 C

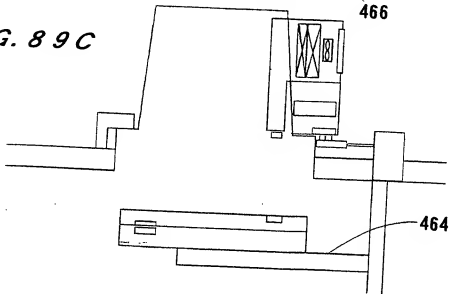


FIG. 90A

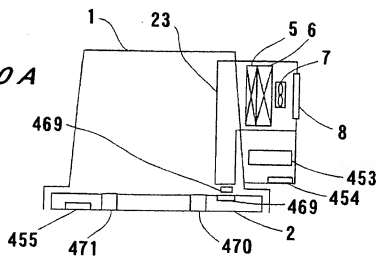


FIG. 90B

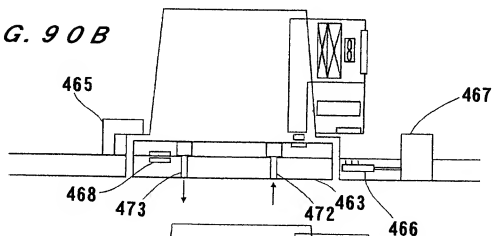


FIG. 90C

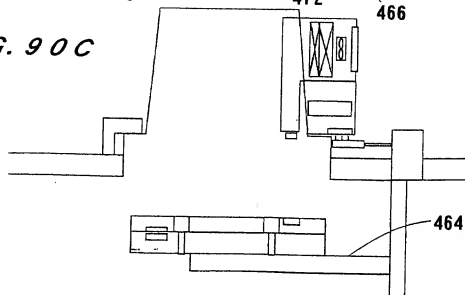
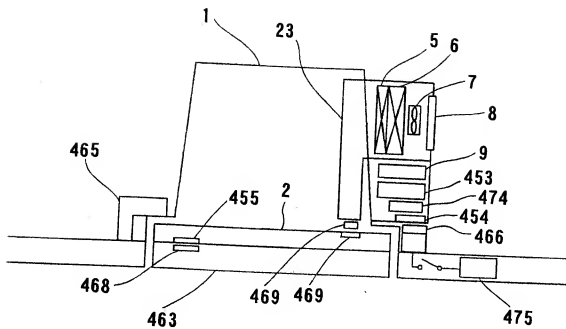
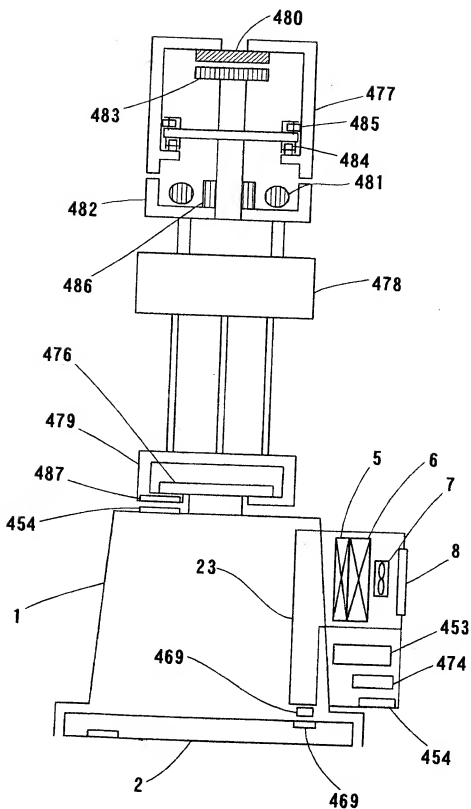


FIG. 91



Job Description



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FIG. 93

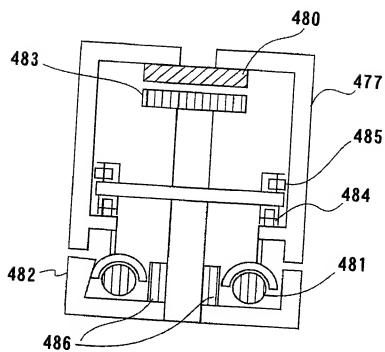


FIG. 94

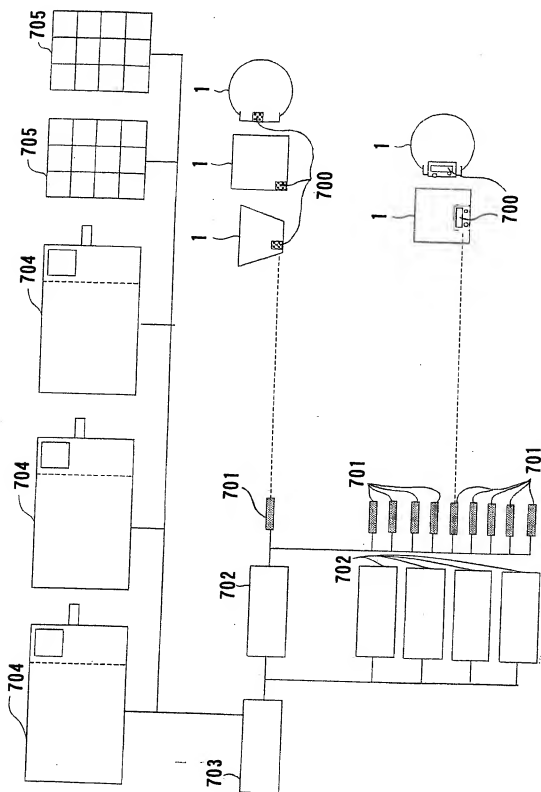


FIG. 95

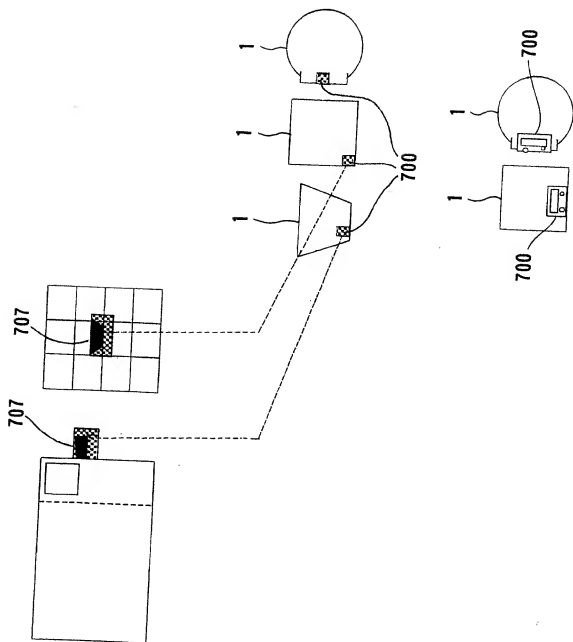
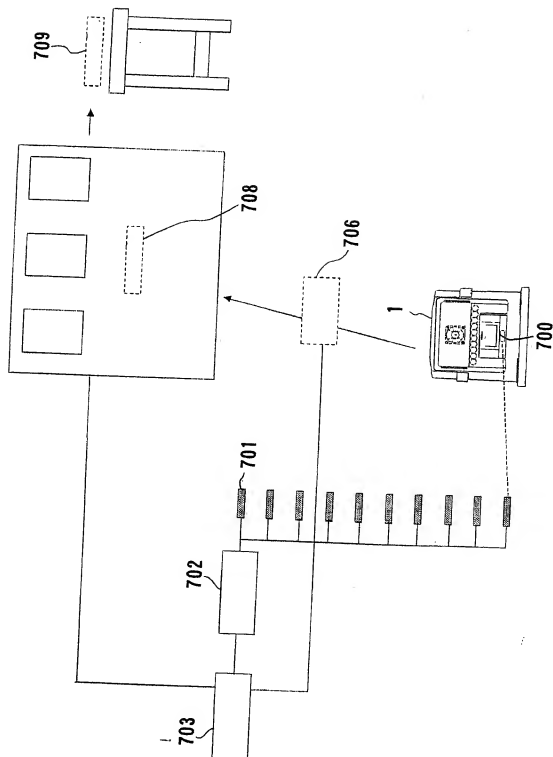


FIG. 96

FIG. 96



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FIG. 97

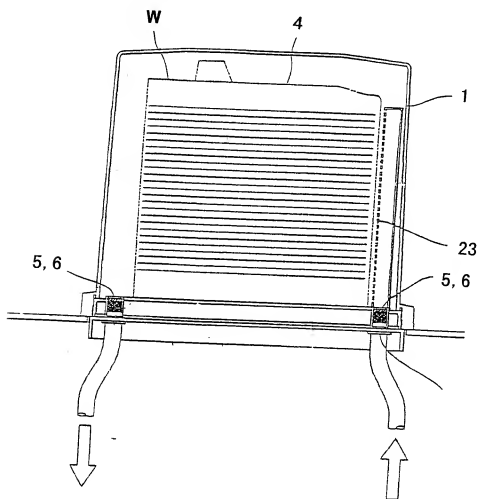
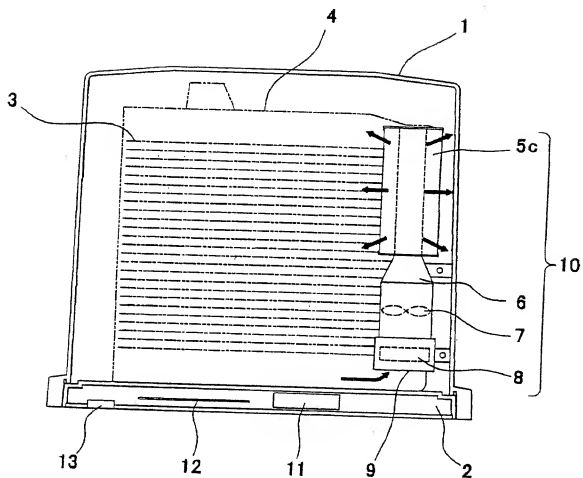


FIG. 98



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FIG. 99

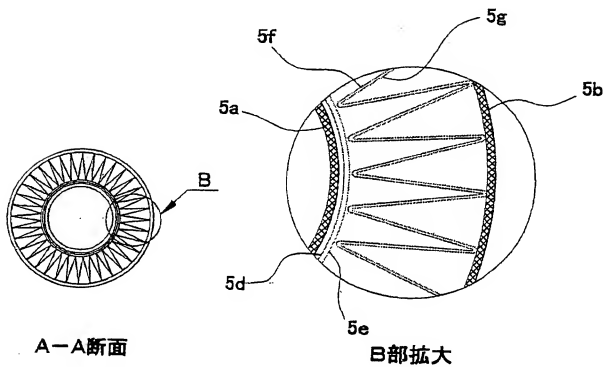
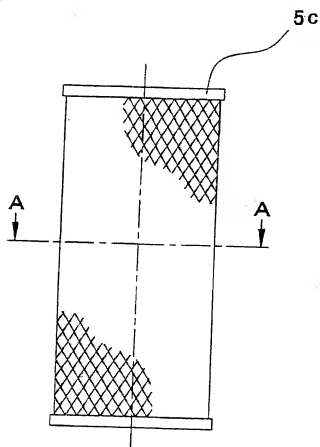


FIG. 100

